

Hanyu Liu

CONTACT INFORMATION	Reconfigurable Computing Laboratory Department of Electrical and Computing Engineering University of Arizona, Tucson, AZ, US, 85721	+1-(520)-626-2104 liuhanyu@ece.arizona.edu
RESEARCH INTERESTS	CAD flow for FPGAs Reconfigurable computing architecture	
EDUCATION	Aug. 2008 - present, MS, Dept. of ECE, University of Arizona, US. Supervisor: Dr. Ali Akoglu Sep. 2003 - Jun. 2007, BS, Dept. of ISEE, Zhejiang University, China.	
HONOURS AND AWARDS	1ST PLACE for National Undergraduate Electronic Design Contest - Embedded System Design Invitational Contest (Intel Cup), Ministry of Education of China, 2006 Certificate of Software Designer, Ministry of Information Industry of China, 2005 Best Idea, Best UI & User Favorite Software for 263 Cup Software Design Contest of Zhejiang University, Zhejiang University, 2005 3rd place for Bird Cup Mobile Phone Software Design Contest, Zhejiang University, 2005	
PUBLICATIONS	Hanyu Liu, Xiaolei Chen and Yajun Ha, <i>An Area-Efficient Timing-Driven Routing Algorithm for Scalable FPGAs with Time-Multiplexed Interconnects</i> , in Proc. IEEE Symp. on Field-Programmable Custom Computing Machines, CA, USA, April, 2008. Hanyu Liu, Xiaolei Chen and Yajun Ha, <i>An Architecture and Timing-Driven Routing Algorithm for Area-Efficient FPGAs with Time-Multiplexed Interconnects</i> , in Proc. the 18th IEEE Symp. on Field Programmable Logic and Applications, Heidelberg, Germany, Sept. 2008.	
PATENT	<i>A Novel Technology on Clutches Linking based on Fuzzy Logic Control</i> , Chinese Patent No: 200510060733.5	
PROFESSIONAL EXPERIENCE	Dept. of ECE, National University of Singapore <i>Teaching Assistant for EE2007 Microprocessor Systems</i> Jan. 2008 - Jun. 2008 VLSI Design Lab, National University of Singapore <i>Research Assistant on FPGA architecture and CAD of FPGAs</i> Aug. 2007 - Jul. 2008 Developed the architecture and routing algorithm for the Time-Multiplexed FPGA whose wire segments are time-shared. Motorola, Shanghai, China <i>Full-time internship for software engineer</i> Jul. 2006 - Oct. 2006 Participated the project of temperature adjusting device for the chair for M-Benz and BMW. National Undergraduate Electronic Design Contest - Embedded System Design Invitational Contest (Intel Cup) <i>Leader of the three-person team</i> Mar. 2006 - Jun. 2006 Performed a P2P streaming video system with self-developed P2P core (Similar to SopCast). Embedded System & Engineering Lab, Zhejiang University <i>Undergraduate research student</i> Jun. 2005 - Dec. 2005 Performed the Automated Mechanical Transmission control for Cherry QQ car.	
TECHNICAL SKILLS	<ul style="list-style-type: none">• Detailed knowledge on FPGA architecture, and CAD flow.• Strong programming skills on C&C++, Matlab, VHDL, Verilog, and Assembly Languages.• Strong experience on Windows programming, MFC, and Linux programming• Experience in Cadence Spectre and Orcad, and Synopsys Tools• Familiar with TCP/IP protocol, and H.264 coding standards	