

Chad T. Rossmeissl

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SKILLS

- Programming Languages: Verilog, C, Visual Basic, MatLab, Perl, Assembly, Java, SmallTalk
- Hardware Platforms: IBM, Sun Workstation, Apple
- Operating Systems: MS Windows, Solaris, Linux, Mac OSX
- Computer Software: Xilinx, Altera MAX Plus II, PSpice, MatLab, ADS, Rational Rose, Eclipse, CodeWarrior, MS Office Suite
- Laboratory: Automated Test Equipment (ATE), Wave Generator, Network Analyzer, Logic Analyzer, Oscilloscope, Multimeter, Soldering, Wire Wrapping

EMPLOYMENT

John Deere, Charlotte, North Carolina

Intern Product Engineer (January 2008—August 2008)

- Developed Hardware and Software for Logging Controller Area Network (CAN) Communications on Commercial Vehicles
- Managed Performance and Reliability Field Tests
- Collected and Processed Field Test Data

Micron Technology, Boise, Idaho

Intern Electrical Engineer (Summer 2006, Summer 2007)

- Developed production test strategies and programs
- Set up and performed production tests in clean room environment
- Reduced test time and variance via Gage Repeatability and Reproducibility (Gage R&R) Study
- Statistical Process Control (SPC)

EDUCATION

The University of Arizona, Tucson, Arizona

August 2008 — Present

- M.S. Computer Engineering
- Focus in Reconfigurable Computing and Control Systems
- Advisor: Dr. Ali Akoglu
- Relevant Courses in: Computer-Aided Logic Design

August 2003 — December 2007

- B.S. Electrical Engineering
- Minor in Computer Engineering and Mathematics
- GPA: 3.57; Major GPA: 3.88
- Relevant Courses in: Digital Logic, Computer Architecture and Design, Circuit Design and Analysis, Device Physics, Object-Oriented Software Design, Microcontrollers (Embedded Systems), Microwave Measurements

PROJECTS

- Team Captain – Senior Design Project, Design and implement a digital thermostat using available TI components including the TI MSP430 microcontroller (Spring, Fall 2007)
- Design and synthesis of MIPS datapath in Verilog, on Spartan 3 FPGA, with Xilinx ISE (Spring 2007)
- Team Captain – Implementation of a Turbocharger Wastegate Controller using a Motorola HCS12 microcontroller (Fall 2005)

References available upon request